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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Applicati n Number	09/742,171
	Filing Date	12/19/00
	First Named Inventor	Steven Teig
	Group Art Unit	2825
	Examiner Name	A.M. Thompson
Total Number of Pages in This Submission	Attorney Docket Number	SPLX.P0017

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm Or Individual name	Mani Adeli, Reg. No. 39,585 of Stattler Johansen & Adeli LLP
Signature	
Date	January 27, 2004

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PATENT
Docket No. SPLX.P0017

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Mani Adeli

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig

Serial No.: 09/742,171

Filing Date: 12/19/00

For: METHOD AND APPARATUS FOR
QUANTIFYING THE QUALITY OF
PLACEMENT CONFIGURATIONS IN
A PARTITIONED REGION OF AN
INTEGRATED-CIRCUIT LAYOUT

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**INFORMATION DISCLOSURE
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Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit with this Information Disclosure Statement the attached Form PTO-1449, and copies of the documents listed in the 1449 form for consideration by the Examiner. The Examiner is requested to make the documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

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This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

This Information Disclosure Statement is submitted after receipt of a first Office Action on the merits but before the Final Office Action. Accordingly, a fee is required. The requisite fee is attached.

However, in the unlikely event that the Patent Office determines that additional fees, extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 50-1128** referencing SPLX.P0017. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: January 27, 2004

Respectfully submitted,

By: _____

Mani Adeli

Registration No. 39,585

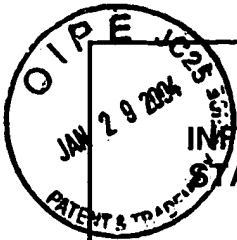
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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number 09/742,171 Filing Date 12/19/00 First Named Inventor Steven Teig Group Art Unit 2825 Examiner Name A.M. Thompson				Sheet	1	of	7
				Attorney Docket Number SPLX.P0017			

U.S. PATENT DOCUMENTS

Examiner* Initials	Cite No. ¹	U.S. Patent Document Number	Kind Code ² (if known)	Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	2001/0009 031	A1	07-19-2001	Nitta et al.	
	2.	2002/0182 844	A1	12-05-2002	Igarashi et al.	
	3.	2003/0025 205	A1	02-06-2003	Shively	
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	5.	4,593,363		06-03-1986	Burstein et al.	
	6.	5,267,176		11-30-1993	Antreich et al.	
	7.	5,587,923		12-24-1996	Wang	
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	13.	5,757,089		05-26-1998	Ishizuka	
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	19.	6,070,108		05-30-2000	Andreev et al.	
	20.	6,128,767		10-03-2000	Chapman	

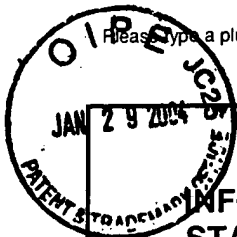
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STATEMENT BY APPLICANT**

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Sheet	2	of	7	Application Number	09/742,171
				Filing Date	12/19/00
				First Named Inventor	Steven Teig
				Group Art Unit	2825
				Examiner Name	A.M. Thompson
				Attorney Docket Number	SPLX.P0017

U.S. PATENT DOCUMENTS

	21.	6,209,123	B1	03-27-2001	Maziasz et al.	
	22.	6,253,363	B1	06-26-2001	Gasarov et al.	
	23.	6,295,634	B1	09-25-2001	Matsumoto	
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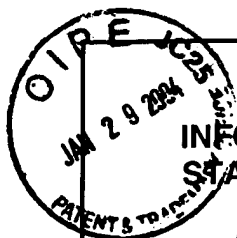
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Substitute for form 1449A/PTO				Applicant Number	09/742,171
				Filing Date	12/19/00
				First Named Inventor	Steven Teig
				Group Art Unit	2825
				Examiner Name	A.M. Thompson
				Attorney Docket Number	SPLX.P0017
Sheet	3	of	7		

FOREIGN PATENT DOCUMENTS

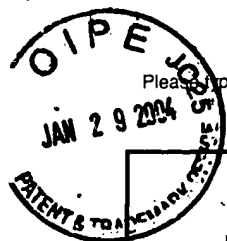
Examiner* Initials	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T 6
		Office ³	Number ⁴	Kind Code (if known)				
	42.	JP	H03-173471	A	Tawada et al.	07-26-1991	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	43.	JP	H05-243379	A	Masako Kubota	09-21-1993	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	44.	JP	H05-102305	A	Akihiro Sato	04-23-1993	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	45.	JP	H07-86407	A	Shinpei Miura	03-31-1995	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	46.	JP	H09-162279	A	Masaaki Yoshida	06-20-1997	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	47.	JP	411296560	A	Matsumoto et al.	10-29-1999	with English translation of Abstract;	

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				Group Art Unit	2825
				Examiner Name	A.M. Thompson
Sheet	4	of	7	Attorney Docket Number	SPLX.P0017

FOREIGN PATENT DOCUMENTS								
	48.	JP	2000-82743	A	Igarashi et al.	03-21-2000	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓

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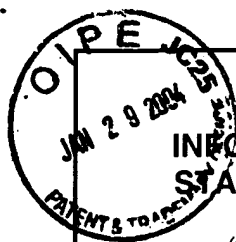
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Sheet	5	of	7	Attorney Docket Number	SPLX.P0017

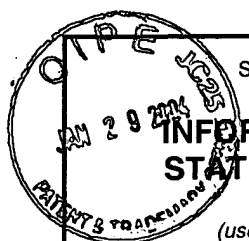
NON PATENT LITERATURE DOCUMENTS					
Examiner* Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T 6
	49.	Ahuja, R. et al., Faster Algorithms for the Shortest Path Problem, Journal of the Association for Computing Machinery, vol. 37, No. 2, April 1990, pp. 213-223.			
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	59.	Enbody, R. et al., Near-Optimal <i>n</i> -Layer Channel Routing, 23 rd Design Automation Conference, 1986, pp. 708-714.			
	60.	Fang, S. et al., Constrained Via Minimization with Practical Considerations for Multi-Layer VLSI/PCB Routing Problems, 28 th ACM/IEEE Design Automation Conference, 1991, pp. 60-65.			
	61.	Hom, I. et al., Estimation of the Number of Routing Layers and Total Wirelength in a PCB Through Wiring Distribution Analysis, 1996, pp. 1-6.			

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Group Art Unit				2825	
Examiner Name				A.M. Thompson	
Attorney Docket Number				SPLX.P0017	
Sheet	6	of	7		

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62.	Hong, X. et al., Performance-Driven Steiner Tree Algorithms for Global Routing, 30 th ACM/IEEE Design Automation Conference, 1993, pp. 177-181.
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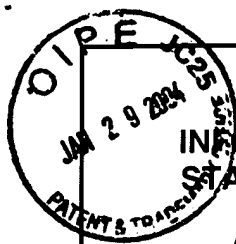
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				Examiner Name	A.M. Thompson
Sheet	7	of	7	Attorney Docket Number	SPLX.P0017

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